



Brillidea

CaS LINEAR

Rev: 0 (4/8/2008)

Sheet: 1/1

7. LED1 & LED2 are to be mounted so the light shines through a hole in the PCB.
6. UDD, for the CY8C201A0 can be 2.7V to 6.0V DC.
5. J2 is not connected to anything. J2 is for mechanical reasons only, see the PCB.
4. J1 is compatible with the Cypress Semiconductor CY3240-I2USB device. A solder pad is provided to connect pin 3 to XRES of IC1 if required.
3. C3 is optional. Adding a capacitor can help with SNR, see PSoC datasheet and Cypress Semiconductor for further information.
2. C1 & C2 are decoupling capacitors for IC1. The capacitors should be placed as close as possible to IC1 on the PCB. C2 is optional and the value can vary.
1. The PSoC can be configured for pulling the I2C lines high or an external pullup can be used.